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APPLICANTS Torayuki Tsukada, Kyoto, JAPAN; ** CONTINUING DATA ***** (Yes) This application is a 371 of PCT/JP03/07457 06/12/2003 ** FOREIGN APPLICATIONS ***** (Yes) JAPAN 2002-177970 06/19/2002 JAPAN 2002-177971 06/19/2002 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/12/2006					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <u> </u> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 2
ADDRESS 23552					
TITLE Chip resistor having low resistance and its producing method					
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		